

## **REMARKS**

### **I. Introduction**

Claims 1 and 2 have been amended. Claims 3 and 4 have been added. No new matter has been added. Claims 1 to 4 are pending. In view of the foregoing amendments and following remarks, it is respectfully submitted that all of the presently pending claims are allowable, and reconsideration is respectfully requested.

Applicants note with appreciation the acknowledgement of the claims for foreign priority and the indication that all of the copies of the certified copies of the priority documents have been received.

Applicants thank the Examiner for considering the previously filed Information Disclosure Statements, PTO-1449 papers, and cited references.

### **II. Objection to Claims 1 and 2**

Claims 1 and 2 have been amended herein without prejudice to obviate the present objection. Withdrawal of the present objection is therefore respectfully requested.

### **III. Rejection of Claims 1 and 2 Under 35 U.S.C. § 102(b)**

Claims 1 and 2 were rejected under 35 U.S.C. § 102(b) as anticipated by Automatic Allocation of Arrays to Memories in FPGA Processors With Multiple Memory Banks by Gokhale et al. (“Gokhale”). It is respectfully submitted that Gokhale does not anticipate the present claims for the following reasons.

Claim 1 relates to a method for partitioning large computer programs and or algorithms at least part of which is to be executed by an array of reconfigurable units. Gokhale is unrelated to such a partitioning. Instead, Gokhale concerns mapping an application’s data to memories. For example, Gokhale at section 5.1 refers to mapping variables onto memories. Mapping data of an application does not disclose partitioning the application itself. Indeed, data of an application is not executed, but is rather read and written. Instructions are executed. Nowhere does Gokhale refer to partitioning of an executable program or algorithm, regardless of the use of the word “partition” in a line of code in section 5.1.

Further, claim 1 recites providing a mapping, not exceeding the maximum allowable size, onto the array of reconfigurable units which execute at least part of the programs and/or algorithms. Gokhale, on the other hand, refers to a mapping of data onto memories. Memories are not units which execute programs or algorithms.

For all of the foregoing reasons, Gokhale does not disclose, or even suggest, all of the features recited in claim 1, so that Gokhale does not anticipate claim 1.

Claim 2 includes subject matter analogous to that of claim 1, so that Gokhale does not anticipate claim 2 for at least the same reasons set forth above in support of the patentability of claim 1.

Withdrawal of this anticipation rejection of claim 1 and 2 is therefore respectfully requested.

**IV. New Claims 3 and 4**

New claims 3 and 4 have been added herein. It is respectfully submitted that new claims 3 and 4 do not add any new matter and are fully supported by the present application, including the specification.

Claim 3 includes subject matter analogous to that of claim 1, so that claim 3 and its dependent claim 4 are therefore allowable for at least essentially the same reasons as claim 1.

**V. Conclusion**

It is respectfully submitted that all of the presently pending claims are in condition for allowance. Prompt reconsideration and allowance of the present application are therefore earnestly solicited.

Respectfully submitted,

Dated: September 17, 2008

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